


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Substitute for form 1449A/PTO				<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				Application Number	09/473,575
				Filing Date	December 28, 1999
				First Named Inventor	Darrell D. Boggs
				Group Art Unit	<del>2755-2127</del>
				Examiner Name	Unknown
Sheet	3	of	3	Attorney Docket Number	042390.P6871



## U.S. PATENT DOCUMENTS

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## FOREIGN PATENT DOCUMENTS

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Examiner Signature	<i>W. L. L. L.</i>	Date Considered	9/3/03
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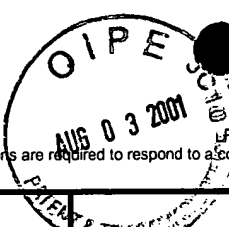
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# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

**Complete if Known**

Sheet 3 of 4

Application Number 09/473,575  
Filing Date December 28, 1999  
First Named Inventor Darrell D. Boggs  
Group Art Unit 2755-2127  
Examiner Name Unknown  
Attorney Docket Number 42390P6871

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## **U.S. PATENT DOCUMENTS**

Exam. Initials	Cite No.	Document Number	Date	Name	Class	Sub-class	Filing Date (if appropriate)
LV ↓ V		5,357,617	10/18/94	Davis et al.	—	—	
		5,524,263	06/04/96	Griffith et al.	—	—	
		5,791,522	08/11/98	Lee et al.	—	—	
		5,996,085	11/30/99	Cheong et al.	—	—	
		5,999,932	12/07/99	Paul	—	—	
		6,052,709	04/18/00	Paul	—	—	
		6,115,709	09/05/00	Gilmour et al.	—	—	
		5,809,271	09/15/98	Colwell et al.	—	—	

## **FOREIGN PATENT DOCUMENTS**

Exam. Initials	Cite No.	Document Number	Date	Country	Class	Sub-class	Translation Yes	No
LV ↓ V		EP0725335A1	19/Jan/96	EPO	—	—		
		EP0747816A2	31/May/96	EPO	—	—		
		EP0827071A2	27/Aug/97	EPO	—	—		
		EP0346003A2	01/Jun/89	EPO	—	—		
		WO99/21082	29/Apr/99	PCT	—	—		
		WO99/21088	29/Apr/99	PCT	—	—		

Examiner Signature	<i>W. Hanro</i>	Date Considered	9/3/03
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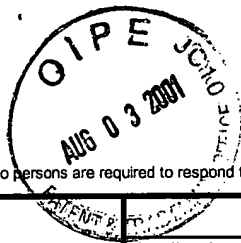
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				Application Number	09/473,575
				Filing Date	December 28, 1999
				First Named Inventor	Darrell D. Boggs
				Group Art Unit	2755-2127
				Examiner Name	Unknown ✓ 0
Sheet	4	of	4	Attorney Docket Number	42390P6871

OTHER ART (Including Title, Author, Date, Pertinent Pages, etc.)

Exam. Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
LU		R. GURU PRASADH, ET AL., A Benchmark Evaluation of a Multi-Threaded RISC Processor Architecture, 1991, pp 184-191, National Science Foundation and Cray Research Incorporated
		ROBERT A. IANNUCCI, ET AL., Multithreaded Computer Architecture: A Summary of the State of the Art, pp 167-200, Kluwer Academic Publishers, Boston/London/Dordrecht
		Proceedings, The 23rd Annual International Symposium on Computer Architecture, May 22-24, 1996, pp 191-202, ACM SIGARCH, IEEE Computer Society, TCCA, Philadelphia, Pennsylvania
		Proceedings, The 23rd Annual International Symposium on Computer Architecture, May 22-24, 1996, pp 203-212, ACM SIGARCH, IEEE Computer Society, TCCA, Philadelphia, Pennsylvania
		DENNIS LEE, ET AL. & BRAD CALDER, ET AL., Instruction Cache Fetch Policies for Speculative Execution, pp 1-11, Department of Computer Science
		SIMON W. MOORE, Multithreaded Processor Design, 1996, pp 1-141, Kluwer Academic Publishers, Boston/Dordrecht/London
		PETER SONG, Multithreading Comes of Age, July 14, 1997, pp 13-18, Microdesign Resources
		RUEDIGER R. ASCHE, Multithreading for Rookies, July 31, 1998, pp 1-15, Microsoft Developer Network Technology Group
✓		IEEE COMPUTER SOCIETY TECHNICAL COMMITTEE ON COMPUTER ARCHITECTURE, Second International Symposium on High-Performance Computer Architecture, February 3-7, 1996, pp291-301

Examiner Signature	<i>Thamir</i>	Date Considered	9/3/03
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Application Number	09/473,575
Filing Date	December 28, 1999
First Named Inventor	Darrell D. Boggs
Group Art Unit	<del>2151</del> 2127
Examiner Name	Vo
Attorney Docket Number	42390P6871

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